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Haseo



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kazuhiro IKURUMI et al.

Serial No.: 09/826,827

Filed: April 6, 2001

For: METHOD AND APPARATUS FOR ADJUSTING IMPEDANCE OF
MATCHING CIRCUIT

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TECHNOLOGY CENTER 2800

DECLARATION

Assistant Commissioner for Patents,
Washington, D.C.

Sir:

I, Mitsuo WADA, of AOYAMA & PARTNERS, located at IMP Building, 1-3-7, Shiromi, Chuo-ku, Osaka 540-0001, Japan, a translator, herewith declare:

(1) that I know well both the Japanese and English languages;

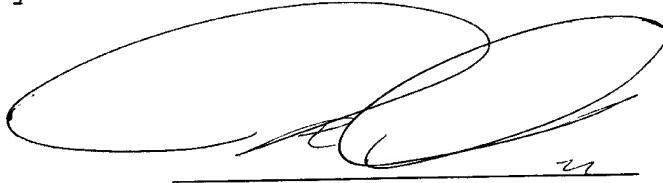
(2) that I am presently engaged as a translator in these languages;

(3) that the attached document represents a true English translation of Japanese Patent Application No. 2000-106563; and

(4) that all statements made of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements are made with the knowledge that willful false

statements and the like are punishable by fine or imprisonment, or both, under 18 USC 1001, and that such false statements may jeopardize the validity of the application or any patent issuing thereon.

Signed this twelfth day of March, 2003.

A handwritten signature in black ink, appearing to read "Mitsuo WADA", is written over a horizontal line. The signature is fluid and cursive, with a large, stylized 'M' and 'W'.

Mitsuo WADA

PATENT OFFICE

JAPANESE GOVERNMENT

This is to certify that the annexed is a true copy of the
following application as filed with this Office.

Date of Application: April 7, 2000

Application Number: Patent Application No. 2000-106563

Applicant(s): Matsushita Electric Industrial Co., Ltd.

April 13, 2001

Kozo OIKAWA

Commissioner,

Patent Office

(seal)

Reg. No. 2001-3030357

Document Name: Application for Patent
Docket No.: 167669
Date of Application: April 7, 2000
Addressee: Commissioner, Patent Office
International Patent Classification: H03H 7/00

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JP2000-106563

Payment of Fees:

Prepayment Book No.: 013262
Amount to be paid: 21,000 yen

Attached document(s):

Item: Specification 1 copy
Item: Drawings 1 copy
Item: Abstract 1 copy

Registration No.
of General Power: 9602660

Request for proof
transmission: Yes



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Kazuhiro IKURUMI et al.

Serial No. 09/826,827

Filed April 6, 2001

Confirmation No. 5095

Docket No. 2001_0409A

Group Art Unit 2817

Examiner Justin P. Bettendorf

METHOD AND APPARATUS FOR
ADJUSTING IMPEDANCE OF
MATCHING CIRCUIT

SUBMISSION OF FORMAL DRAWINGS

Assistant Commissioner for Patents,
Washington, DC 20231

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEES FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975

Sir:

In accordance with Patent Office practice, submitted herewith are formal drawings for
Figs. 1-35 (19 sheets, A4 Paper) as required by the Examiner.

Respectfully submitted,

Kazuhiro IKURUMI et al.

By:

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March 19, 2003

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ACCOUNT NO. 23-0975

LETTER RE PROPOSED DRAWING AMENDMENT

Assistant Commissioner for Patents,
Washington, D.C.

Sir:

Enclosed herewith is a photocopy of Fig. 33 marked in red to indicate proposed drawing amendment thereto.

The Examiner is requested to approve such proposed drawing amendment.

Respectfully submitted,

Kazuhiro IKURUMI et al.

By David M. Ovedovitz
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